

10744/42

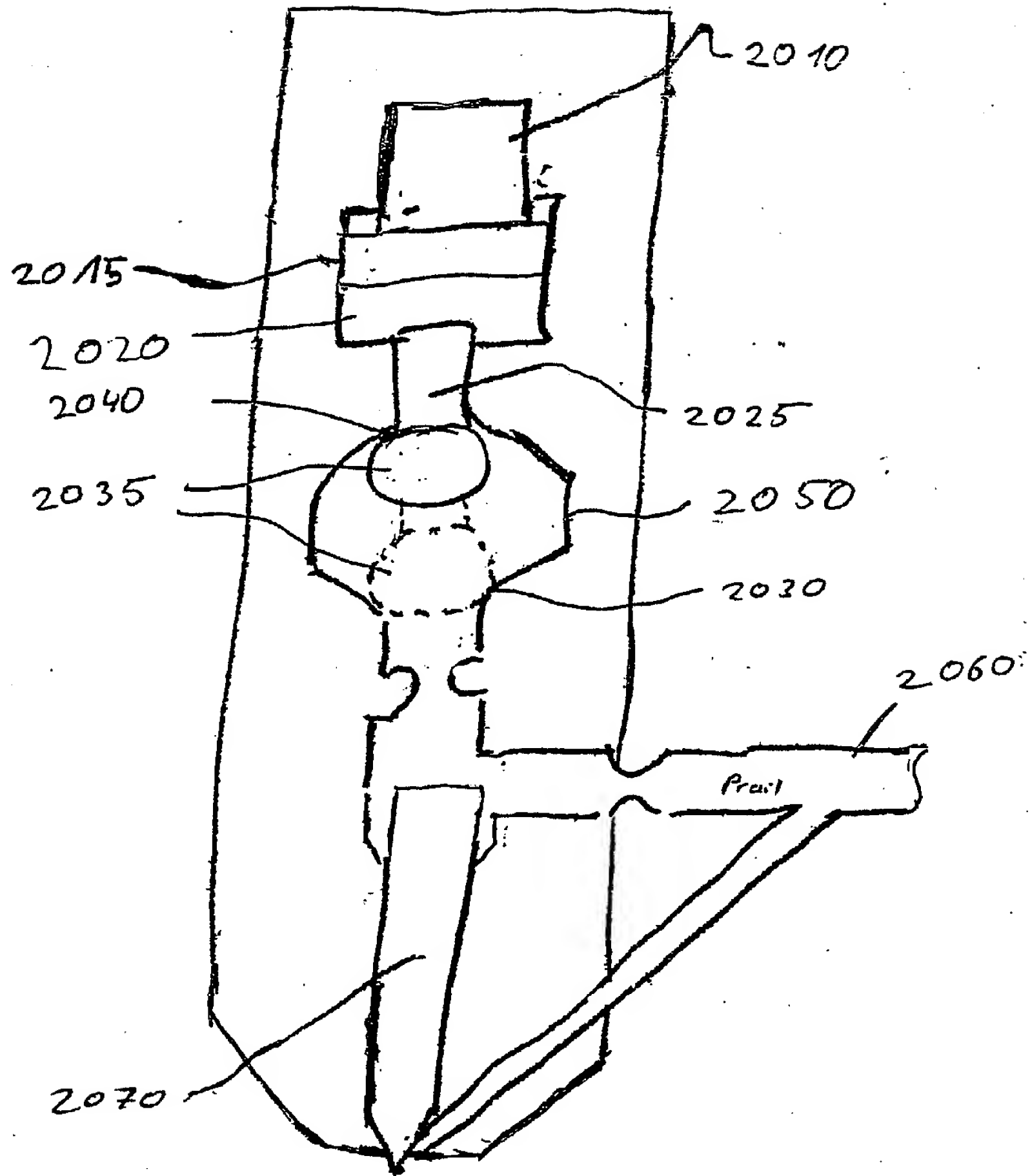
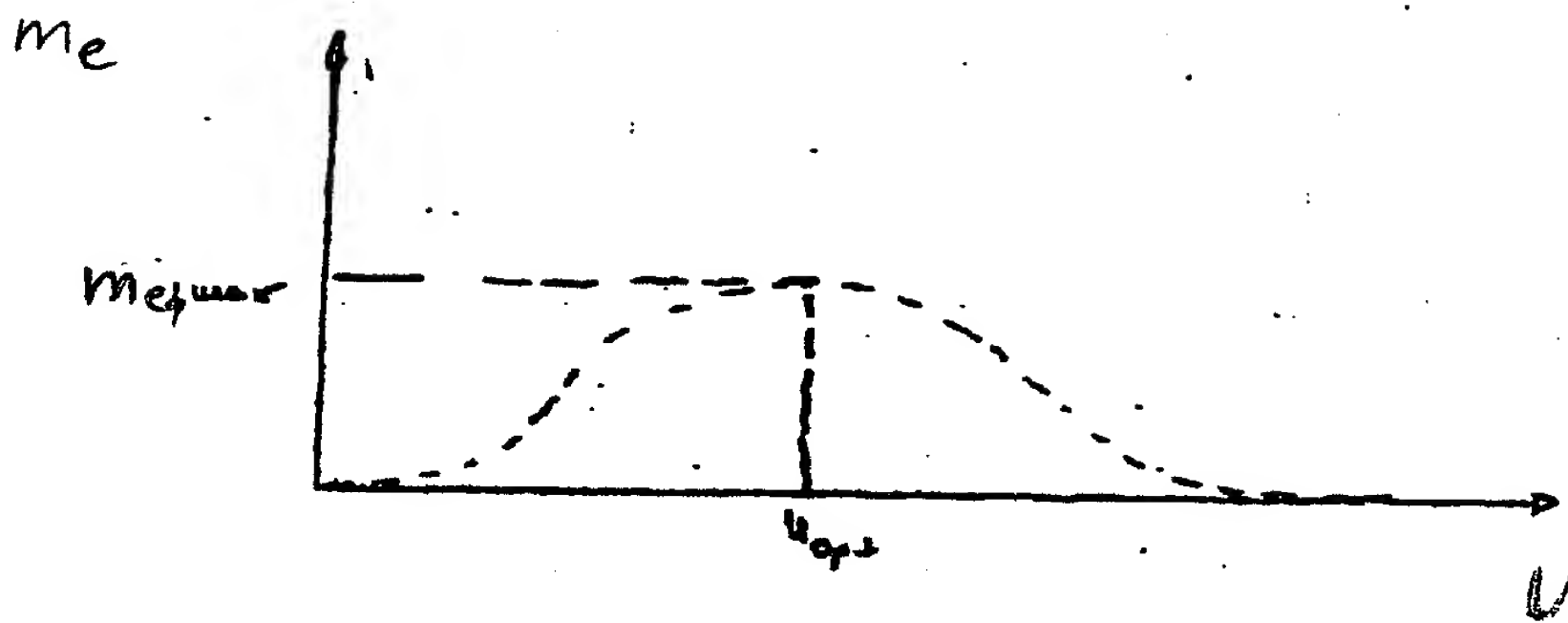


Fig. 7

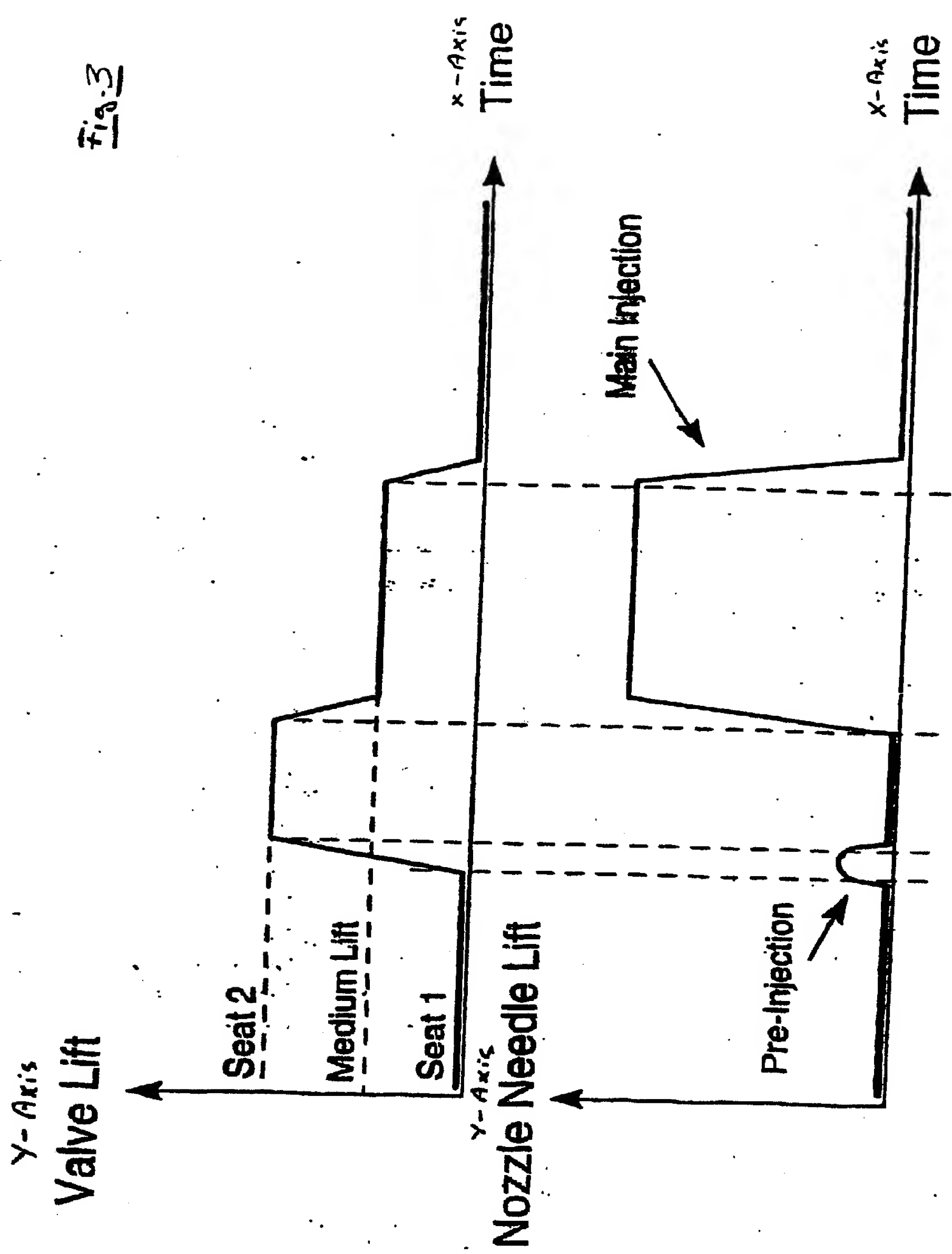
10744/42

Fig-2



10744/42

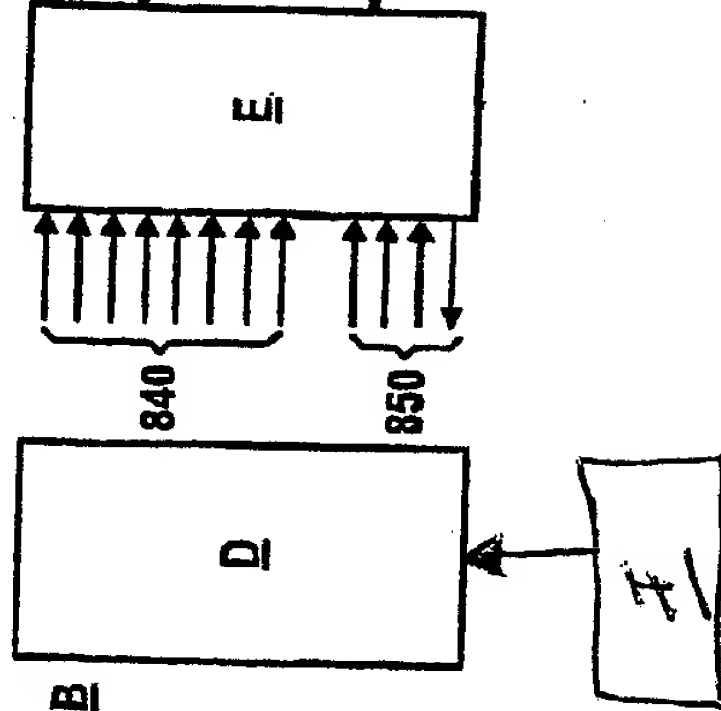
Fig. 3



[illegible]

FIG. 4

The diagram illustrates a semiconductor device, likely a memory array, showing a grid of access transistors (10-64) and data lines (110-160). The device is divided into sections G1, G2, and 600. A cross-section view (710) shows the physical structure of the device, including the word line (730), data line (740), and sense line (750). The device is connected to a power supply (201) and ground (200). The diagram is labeled with various components and their connections, including a word line driver (200-240), a sense amplifier (300-330), and a data line (110-160).



10744/4

FIG. 5a

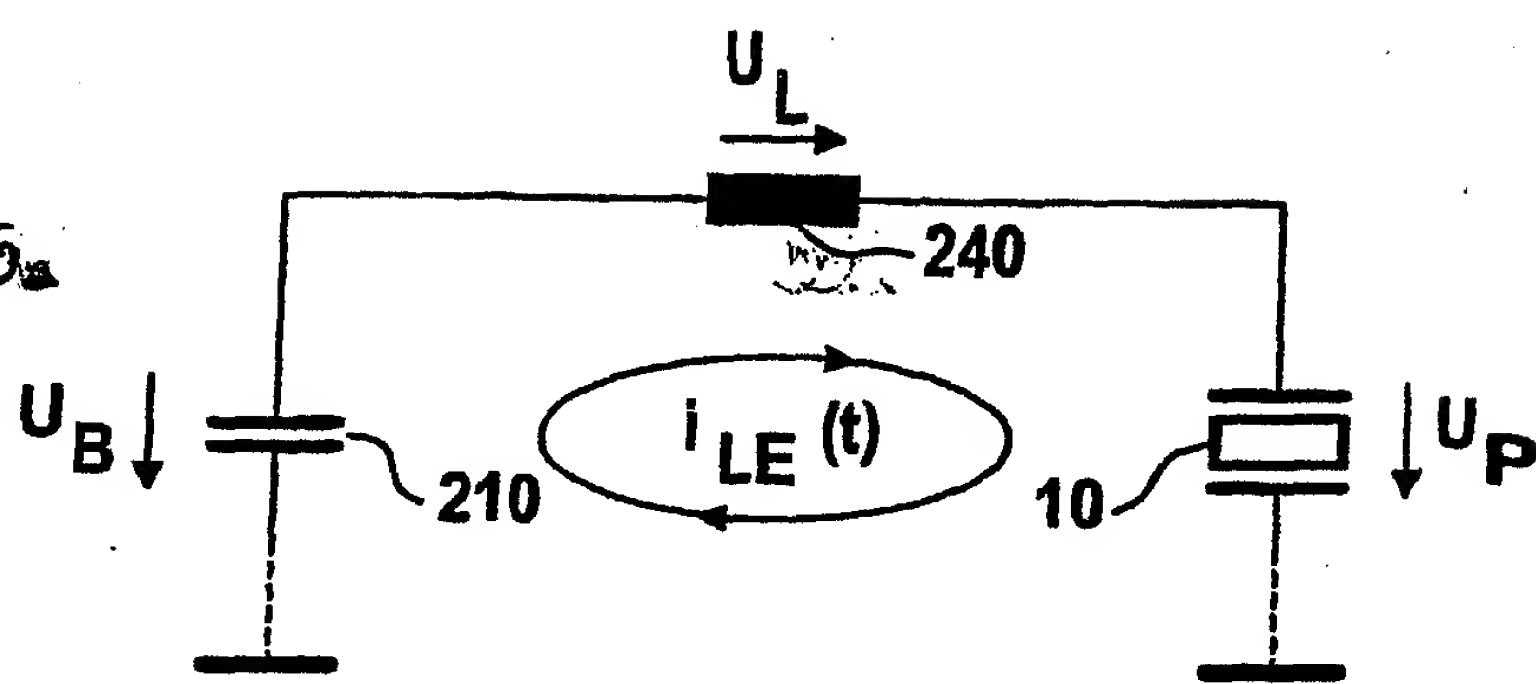


FIG. 5b

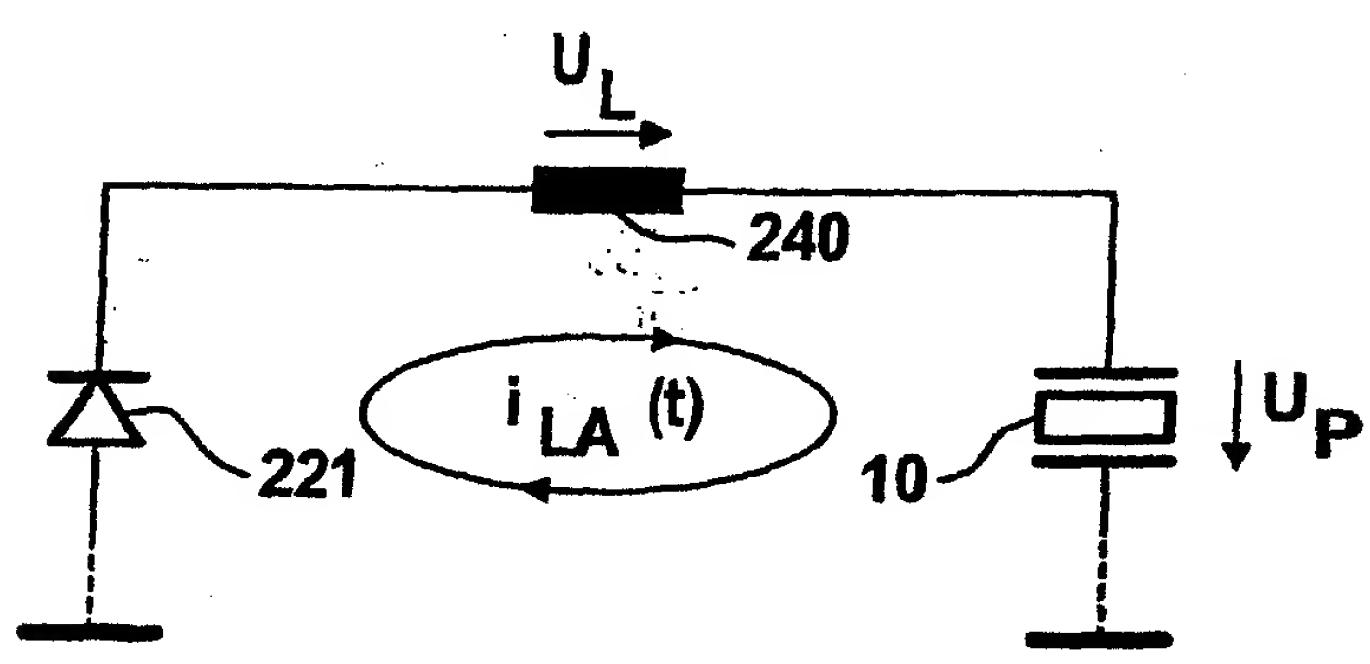


FIG. 5c

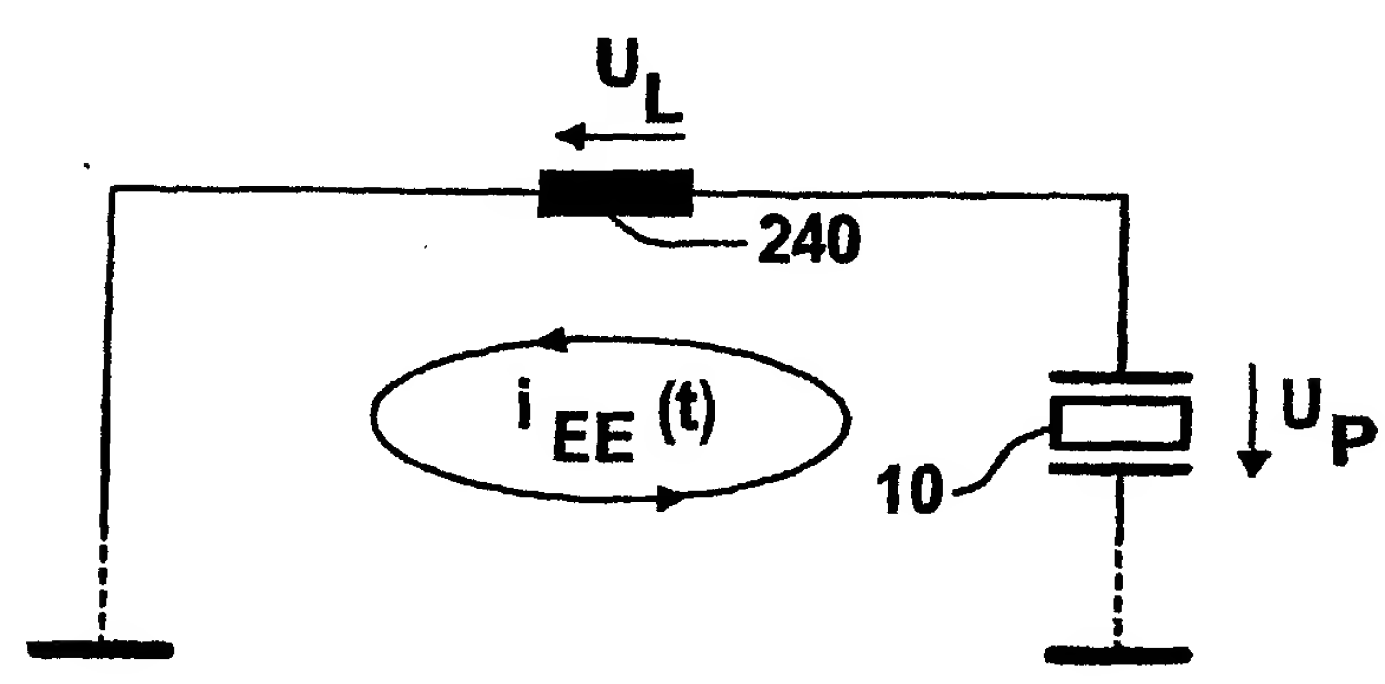
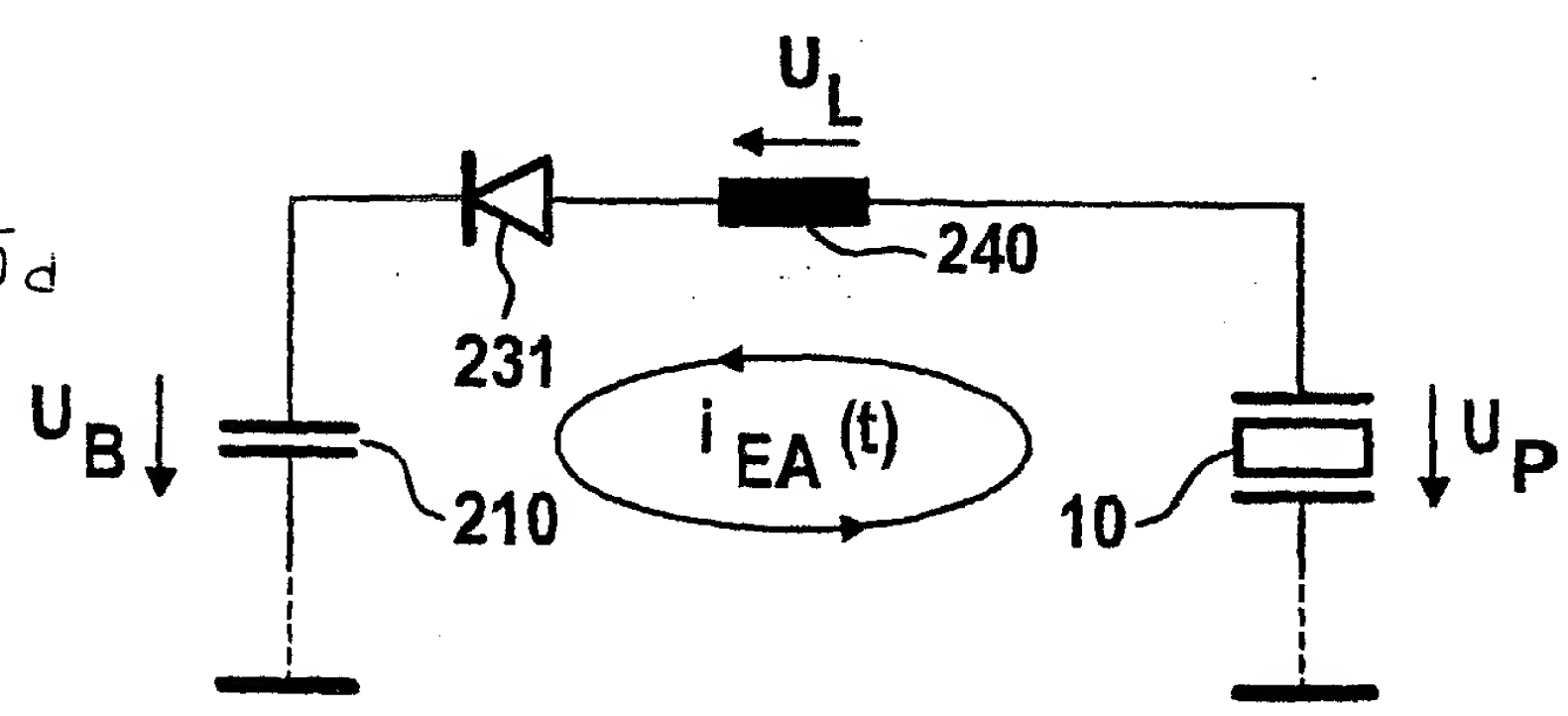


FIG. 5d



10744/

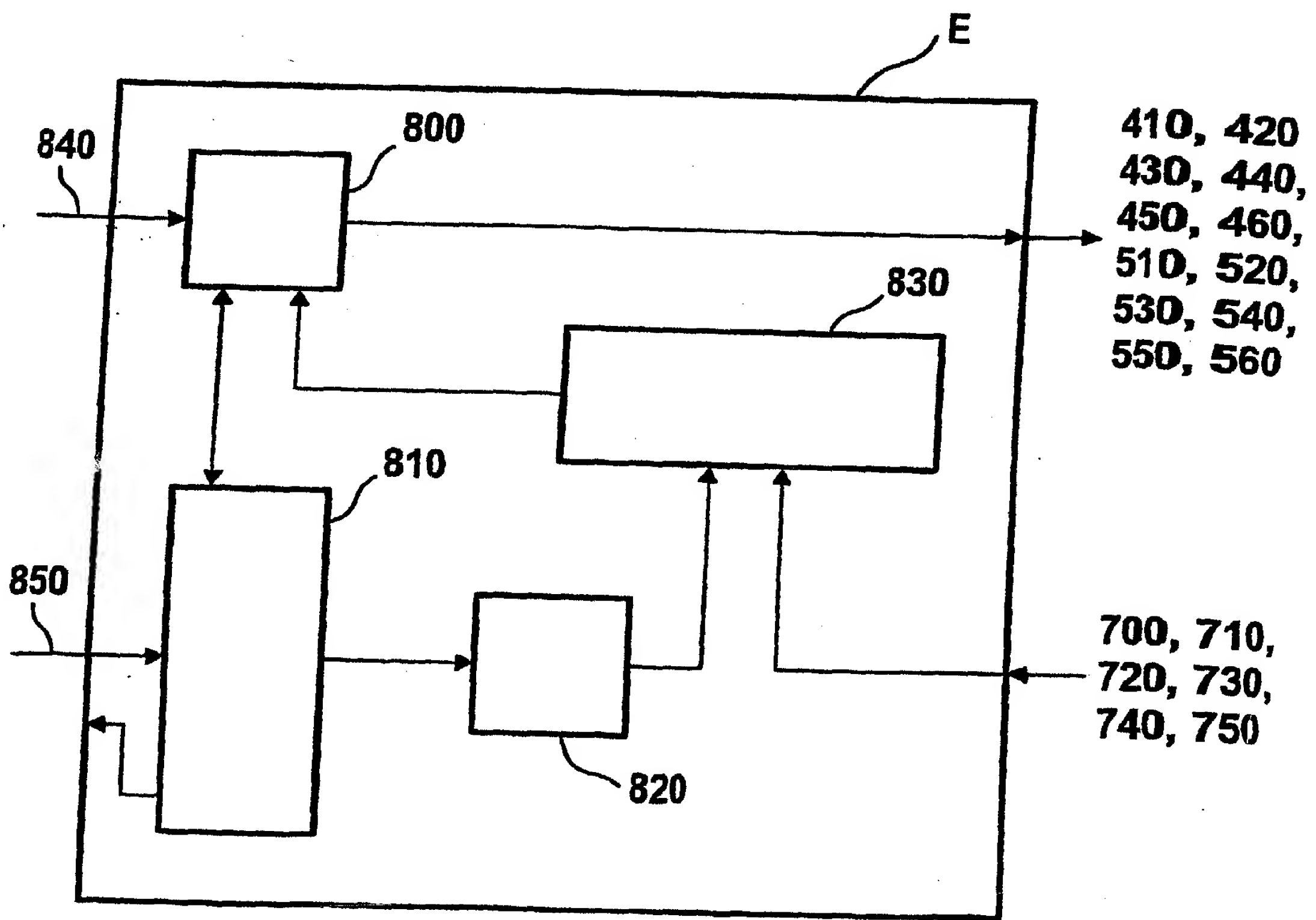


FIG. 6

FOR REFERENCE

Fig. 7

